

MEMORY BUS POLARITY INDICATOR SYSTEM AND METHOD FOR REDUCING
THE AFFECTS OF SIMULTANEOUS SWITCHING OUTPUTS (SSO) ON MEMORY
BUS TIMING

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ABSTRACT OF THE DISCLOSURE

10 A method and system transfer read data from a memory device having a data
bus and a data masking pin adapted to receive a masking signal during write operations of
the memory device. The method includes placing a sequence of read data words on the
data bus and applying a data bus inversion signal on the data masking pin, the data bus
inversion signal indicating whether the data contained each read data word has been
15 inverted. Another method and system transfer data over a data bus. The method includes
generating a sequence of data words, at least one data word including data bus inversion
data. The sequence of data words is applied on the data bus and is thereafter stored. The
data bus inversion data is applied to invert or not invert the data contained in the stored data
words.

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